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INTELLECTUAL PROPERTY LAW

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January 2, 2007

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Re: **CERTIFICATE OF CORRECTION**  
U. S. Letters Patent No. 7,109 774  
Issued: September 19, 2006  
For: DELAY LOCKED LOOP (DLL) CIRCUIT AND METHOD FOR  
LOCKING CLOCK DELAY BY USING THE SAME  
Inventor: Kim  
Our File No. 051876.P560

Dear Sir:

Enclosed is a Certificate of Correction (two copies) for the above-referenced patent.

This request for correction is made under rule 322 of the Rules of Practice and 35 U.S.C. Section 254.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Eric S. Hyman  
Reg. No. 30,139

**Certificate**

FEB 01 2007

**of Correction**

ESH/nsf  
Enclosures:  
PTO Form- 1449

FEB - 2 2007

**UNITED STATES PATENT AND TRADEMARK OFFICE  
CERTIFICATE OF CORRECTION**

**PATENT NO.:** 7,109,774

**DATED:** September 19, 2006

**INVENTOR(S):** Kim

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In Claim #7, Line 24, please delete "through an output".

**MAILING ADDRESS OF SENDER:**  
BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP  
12400 Wilshire Blvd. 7th floor  
Los Angeles, CA 90025-1030

**PATENT NO.** 7,109,774

**Certificate of Correction (PTO Form 1050)-Amended**

1 of 1

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